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Keyw ords: Internet, Web-based CAD, data security, encryption

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Marco Eichelberg, Thomas Aden, Jörg Riesmeier, Asuman Dogac, Gokce B. Laleci

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-  Nicholas Weaver, Yury Markovskiy, Yatish Patel, John Wawrzynek
February 2003 FPGA '03: Proceedings of the 2003 ACM/SIGDA eleventh international symposium on programmable gate arrays
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 Tom Kean
February 2002 FPGA '02: Proceedings of the 2002 ACM/SIGDA tenth international symposium on F programmable gate arrays
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 Xinping Zhu, Sharad Malik
January 2007 ACM Transactions on Design Automation of Electronic Systems (TODAES), volume 12 Issue 1
Publisher: ACM
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In multiprocessor-based SoCs, optimizing the communication architecture is often as important, more important, than optimizing the computation architecture. While there are mature platform techniques for the modeling and evaluation of architectures ...
Keywords: Retargetable simulation, bus, design exploration, multiprocessor system, network-on-chip, object-oriented modeling, on-chip communication architecture, packet-switching network

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